

generating a hardware description based on the graphical program, wherein the hardware description describes a hardware implementation of the graphical program, wherein said generating includes generating a hardware description based on the structure node;

configuring a programmable hardware element utilizing the hardware description to produce a configured hardware element, wherein the configured hardware element implements a hardware implementation of the graphical program;

acquiring a signal from an external source after said configuring; and

the configured hardware element executing to perform the measurement function on the signal.

REMARKS

Applicant is in receipt of the Office Action mailed March 25, 2003. Claim 65 was objected to due to a typographical error, and a double patenting rejection was made with respect to U.S. Patent No. 6,219,628. Applicant has corrected the phrase "A." in claim 65 line 1. Applicant also encloses a terminal disclaimer to obviate the double patenting rejection.